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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/670,715	09/25/2003	David A. Luick	ROC920030293US1	6101
16797 7590 03/06/2007 IBM CORPORATION, INTELLECTUAL PROPERTY LAW DEPT 917, BLDG. 006-1 3605 HIGHWAY 52 NORTH ROCHESTER, MN 55901-7829			EXAMINER	
			ROJAS, MIDYS	
			ART UNIT	PAPER NUMBER
			2185	
			· · · · · · · · · · · · · · · · · · ·	
			MAIL DATE	DELIVERY MODE
			03/06/2007	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

Advisory Action Before the Filing of an Appeal Brief

Application No.	Applicant(s)	
10/670,715	LUICK, DAVID A.	
Examiner	Art Unit	
Midys Rojas	2185	

The MAILING DATE of this communication appears on the cove	r sheet with the correspondence address
THE REPLY FILED 12 February 2007 FAILS TO PLACE THIS APPLICATION IN	N CONDITION FOR ALLOWANCE.
1. The reply was filed after a final rejection, but prior to or on the same day as this application, applicant must timely file one of the following replies: (1) a places the application in condition for allowance; (2) a Notice of Appeal (w a Request for Continued Examination (RCE) in compliance with 37 CFR 1 time periods:	an amendment, affidavit, or other evidence, which vith appeal fee) in compliance with 37 CFR 41.31; or (3)
a) The period for reply expiresmonths from the mailing date of the final re	ejection.
b) The period for reply expires on: (1) the mailing date of this Advisory Action, or (no event, however, will the statutory period for reply expire later than SIX MON	THS from the mailing date of the final rejection.
Examiner Note: If box 1 is checked, check either box (a) or (b). ONLY CHECK TWO MONTHS OF THE FINAL REJECTION. See MPEP 706.07(f).	BOX (b) WHEN THE FIRST REPLY WAS FILED WITHIN
Extensions of time may be obtained under 37 CFR 1.136(a). The date on which the petitic have been filed is the date for purposes of determining the period of extension and the counder 37 CFR 1.17(a) is calculated from: (1) the expiration date of the shortened statutory set forth in (b) above, if checked. Any reply received by the Office later than three months may reduce any earned patent term adjustment. See 37 CFR 1.704(b). NOTICE OF APPEAL	rresponding amount of the fee. The appropriate extension fee period for reply originally set in the final Office action; or (2) as
 The Notice of Appeal was filed on A brief in compliance with 37 Cl filing the Notice of Appeal (37 CFR 41.37(a)), or any extension thereof (37 a Notice of Appeal has been filed, any reply must be filed within the time pamendments 	7 CFR 41.37(e)), to avoid dismissal of the appeal. Since
3. The proposed amendment(s) filed after a final rejection, but prior to the date.	ate of filing a brief will not be entered because
(a) They raise new issues that would require further consideration and/o	
(b) They raise the issue of new matter (see NOTE below);	
(c) They are not deemed to place the application in better form for appearappeal; and/or	eal by materially reducing or simplifying the issues for
(d) They present additional claims without canceling a corresponding nu	umber of finally rejected claims.
NOTE: (See 37 CFR 1.116 and 41.33(a)).	
4. The amendments are not in compliance with 37 CFR 1.121. See attached	Notice of Non-Compliant Amendment (PTOL-324).
5. Applicant's reply has overcome the following rejection(s):	
6. Newly proposed or amended claim(s) would be allowable if submit non-allowable claim(s).	
7. For purposes of appeal, the proposed amendment(s): a) will not be en how the new or amended claims would be rejected is provided below or at The status of the claim(s) is (or will be) as follows:	
Claim(s) allowed: Claim(s) objected to:	1
Claim(s) objected to: Claim(s) rejected:	
Claim(s) withdrawn from consideration:	
AFFIDAVIT OR OTHER EVIDENCE	
 The affidavit or other evidence filed after a final action, but before or on the because applicant failed to provide a showing of good and sufficient reaso was not earlier presented. See 37 CFR 1.116(e). 	
9. The affidavit or other evidence filed after the date of filing a Notice of Appendix entered because the affidavit or other evidence failed to overcome <u>all</u> rejesthowing a good and sufficient reasons why it is necessary and was not earned.	ctions under appeal and/or appellant fails to provide a
10. ☐ The affidavit or other evidence is entered. An explanation of the status of REQUEST FOR RECONSIDERATION/OTHER	* * * * * * * * * * * * * * * * * * * *
11. The request for reconsideration has been considered but does NOT plac See Continuation Sheet.	e the application in condition for allowance because:
12. Note the attached Information Disclosure Statement(s). (PTO/SB/08) Page 12.	per No(s)
13. Other:	Janja
	SANJIV SHAH

SANJIV SHAH SUPERVISORY PATENT EXAMINER TECHNOLOGY CENTER 2100 Continuation of 11. does NOT place the application in condition for allowance because: Applicant argues that Dean does not teach the monitoring of the cache miss rates of more than one processor in order to allocate cache lines of more than one private caches to the processors. Instead of disclosing multiple private caches, Dean discloses a single unified multi-port cache. However, in the system of Dean, if a processor A's miss counter is larger than processor B's miss counter by a predetermined cache reallocation factor, some ways of the cache will be assigned to processor A, wherein cache ways are allocated to each processor so that each group of cache ways represents a particular private cache section for that processor. Therefore, a group of cache ways out of the multi-port cache represents one private cache for each processor. Dean also teaches that another architecturethat can be implemented for a multiple processor system is one where each processor has its own private cache (Col. 2, lines 46-52); consequently, Dean does teach more than one private cache, one for each processor. Therfore, the system of Dean can operate using independent private caches and the dynamic allocation of the invention operates in the same manner as disclosed by Dean with independent caches or with a unified cache. Eventhough the examiner agreed that the cache of Dean is a unified cache, Dean does teach that another architecture that can be implemented is that of multiple private caches, one for each processor. Therefore, the invention of Dean does teach the claimed limitations.